

CDSF4148

List

List.....	1
Package outline.....	2
Features.....	2
Mechanical data.....	2
Maximum ratings	2
Rating and characteristic curves.....	3
Pinning information.....	4
Marking.....	4
Suggested solder pad layout.....	4
Packing information.....	5
Reel packing.....	6
Suggested thermal profiles for soldering processes.....	6
High reliability test capabilities.....	7

CDSF4148

150mA Surface Mount Switching Diode-75V

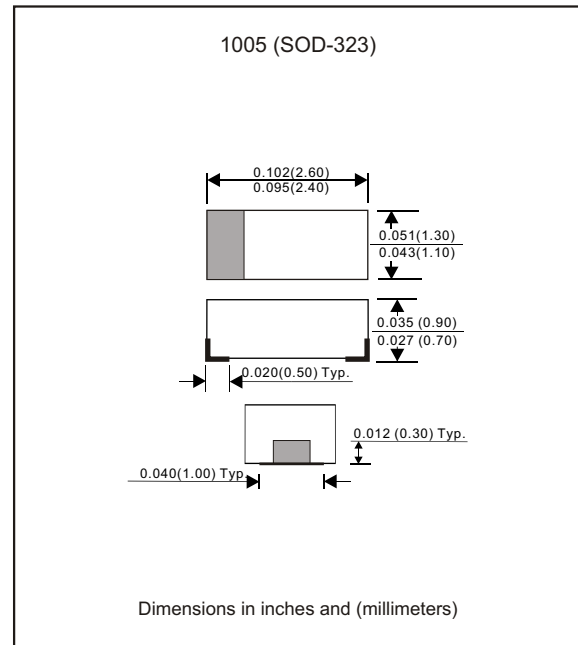
Features

- Batch process design.
- Fast speed switching.
- For general purposeswitching application.
- High conductance.
- Silicon epitaxial planar chip.
- Lead-free parts meet RoHS requirements.

Mechanical data

- Epoxy:UL94-VO rated flame retardant
- Case : Molded plastic, 1005 (SOD-323)
- Terminals : Solder plated, solderable per MIL-STD-750, Method 2026
- Polarity : Indicated by cathode band
- Mounting Position : Any
- Weight : Approximated 0.007 gram

Package outline



Maximum ratings (AT T_A=25°C unless otherwise noted)

PARAMETER	CONDITIONS	Symbol	MIN.	TYP.	MAX.	UNIT
Repetitive peak reverse voltage		V _{RRM}			75	V
Reverse voltage		V _R			75	V
Peak forward surge current	t _p = 1 us	I _{FSM}			1000	mA
Forward current		I _F			150	mA
Average forward current	V _R = 0 V	I _{FAV}			150	mA
Power dissipation		P _V			500	mW
Junction temperature		T _j	-55		+150	°C
Storage temperature		T _{STG}	-65		+175	°C
Forward voltage	I _F = 50 mA	V _F		0.86	1.00	V
Reverse current	V _R = 20 V	I _R			25	nA
	V _R = 75 V	I _R			2.5	uA
Diode capacitance	V _R = 0 V, f = 1MHz, V _{HF} = 50mV	C _D			4.0	pF
Reverse recovery time	I _F = 10 mA, V _R = 6V, I _{RR} = 0.1 X I _R , R _L = 100 _{OHM}	t _{rr}			4	ns

Rating and characteristic curves (CDSF4148)

FIG.1-TYPICAL FORWARD CHARACTERISTICS

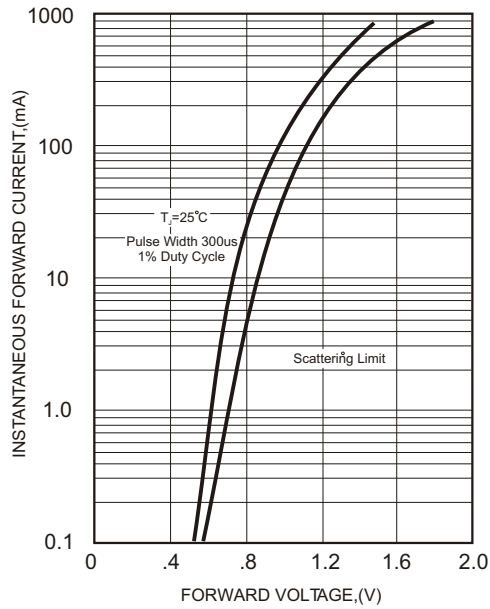


FIG.3 - TYPICAL REVERSE CHARACTERISTICS

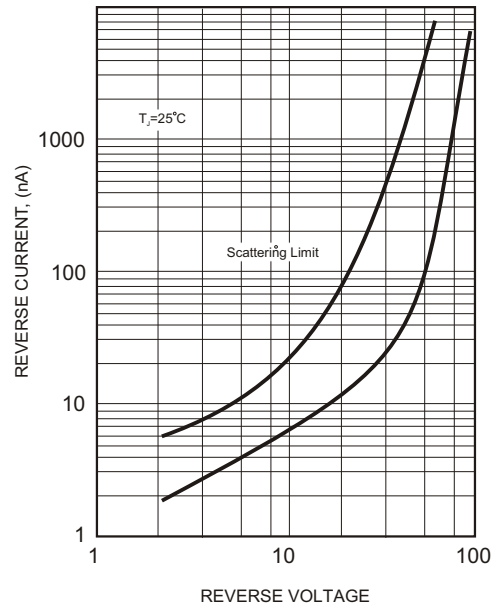


FIG.2 - TYPICAL DIODE CAPACITANCE

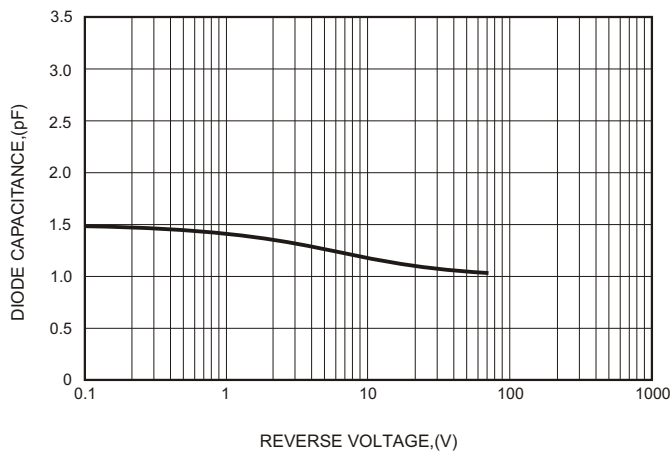
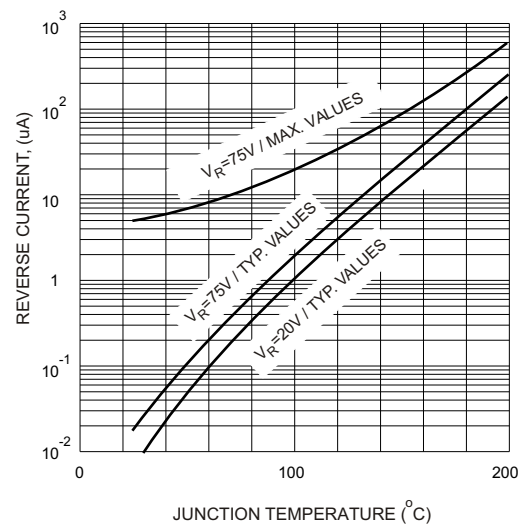




FIG.4 - REVERSE CURRENT VS JUNCTION TEMPERATURE



CDSF4148

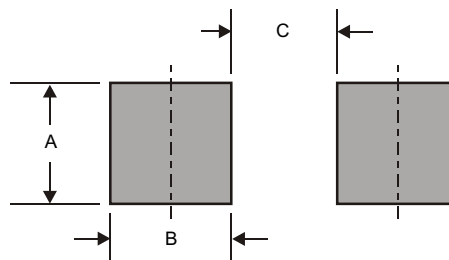
Pinning information

Pin	Simplified outline	Symbol
Pin1 cathode Pin2 anode		

Marking

Type number	Marking code
CDSF4148	-

Suggested solder pad layout

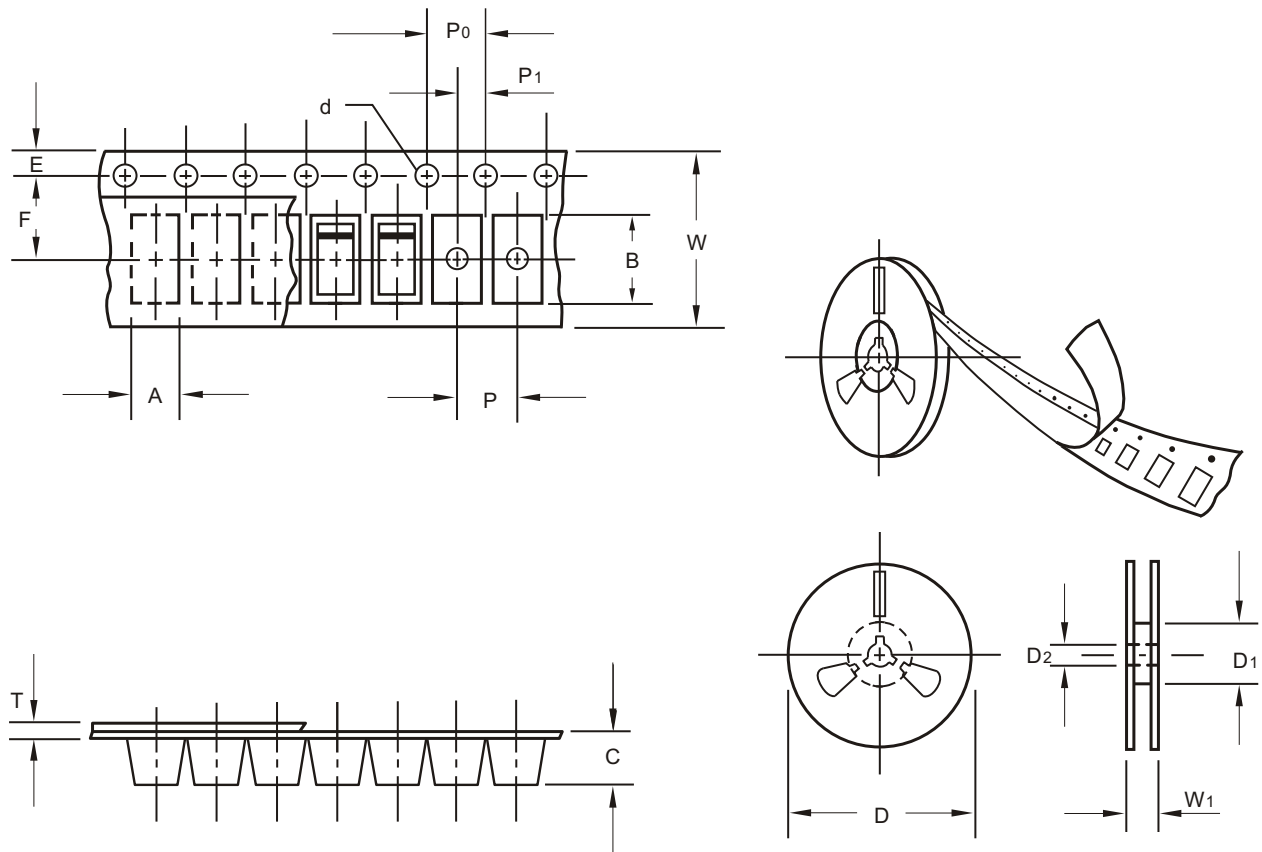


Dimensions in inches and (millimeters)

PACKAGE	A	B	C
1005	0.026(0.65)	0.054(1.35)	0.042(1.05)

CDSF4148

Packing information



unit:mm

Item	Symbol	Tolerance	1005
Carrier width	A	0.1	1.55
Carrier length	B	0.1	2.65
Carrier depth	C	0.1	1.05
Sprocket hole	d	0.1	1.50
7" Reel outside diameter	D	2.0	178.00
7" Reel inner diameter	D1	min	50.00
Feed hole diameter	D2	0.5	13.00
Sprocket hole position	E	0.1	1.75
Punch hole position	F	0.1	3.50
Punch hole pitch	P	0.1	4.00
Sprocket hole pitch	P0	0.1	4.00
Embossment center	P1	0.1	2.00
Overall tape thickness	T	0.1	0.23
Tape width	W	0.3	8.00
Reel width	W1	1.0	11.40

Note: Devices are packed in accordance with EIA standard RS-481-A and specifications listed above.

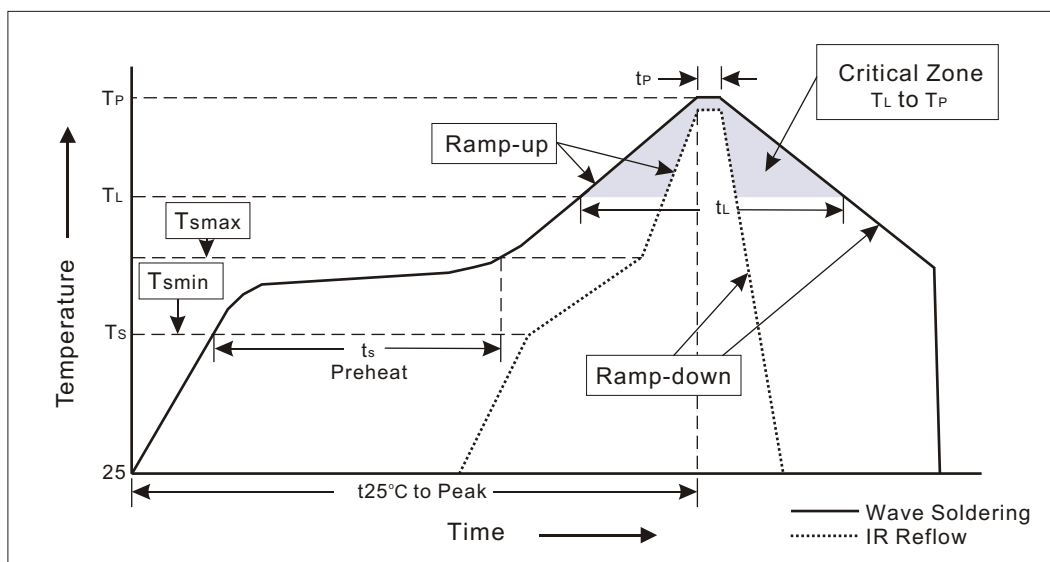
CDSF4148

Reel packing

PACKAGE	REEL SIZE	REEL (pcs)	COMPONENT SPACING (m/m)	BOX (pcs)	INNER BOX (m/m)	REEL DIA, (m/m)	CARTON SIZE (m/m)	CARTON (pcs)	APPROX. GROSS WEIGHT (KG)
1005	7"	4000	4.0	20,000	180*180*60	178	370*350*200	200,000	6.4

Suggested thermal profiles for soldering processes

- 1.Storage environment: Temperature=10°C~35°C Humidity=65%±15%
- 2.Reflow soldering of surface-mount devices



3.Flow (wave)soldering (solder dipping)

Profile Feature	Soldering Condition
Average ramp-up rate(T_L to T_P)	<3°C/sec
Preheat -Temperature Min(T_{Smin}) -Temperature Max(T_{Smax}) -Time(min to max)(t_s)	100°C 150°C 60~120sec
T_{Smax} to T_L -Ramp-upRate	<3°C/sec
Time maintained above: -Temperature(T_L) -Time(t_L)	183°C 60~150sec
Peak Temperature(T_P)	255°C-0/+5°C
Time within 5°C of actual Peak Temperature(t_P)	10~30sec
Ramp-down Rate	<6°C/sec
Time 25°C to Peak Temperature	<6minutes

CDSF4148**High reliability test capabilities**

Item Test	Conditions	Reference
1. Solder Resistance	at 260±5°C for 10±2sec. immerse body into solder 1/16"±1/32"	MIL-STD-750D METHOD-2031
2. Solderability	at 245±5°C for 5 sec.	MIL-STD-202F METHOD-208
3. High Temperature Reverse Bias	$V_R=80\%$ rate at $T_A=150^\circ\text{C}$ for 168 hrs.	MIL-STD-750D METHOD-1026
4. Forward Operation Life	Rated average rectifier current at $T=25^\circ\text{C}$ for 500hrs.	MIL-STD-750D METHOD-1027
5. Intermittent Operation Life	$T_A = 25^\circ\text{C}$, $I_F = I_O$ On state: power on for 5 min. off state: power off for 5 min. on and off for 500 cycles.	MIL-STD-750D METHOD-1036
6. Pressure Cooker	15P _{sig} at $T_A=121^\circ\text{C}$ for 4 hrs.	
7. Temperature Cycling	-55°C to +125°C dwelled for 30 min. and transferred for 5min. total 10 cycles.	MIL-STD-750D METHOD-1051
8. Thermal Shock	0°C for 5 min. rise to 100°C for 5 min. total 10 cycles.	MIL-STD-750D METHOD-1056
9. Forward Surge	8.3ms single half sine-wave superimposed on rated load, one surge.	MIL-STD-750D METHOD-4066-2
10. Humidity	at $T_A=65^\circ\text{C}$, RH=98% for 1000hrs.	MIL-STD-750D METHOD-1038
11. High Temperature Storage Life	at 175°C for 1000hrs.	MIL-STD-750D METHOD-1031
12. Solvent Resistance	Dip into Freon at 25°C for 1 min.	MIL-STD-202F METHOD-215